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APPLICANTS
 Kazutaka Shibata, Kyoto, JAPAN;

** CONTINUING DATA ***** *None*

** FOREIGN APPLICATIONS *****
 JAPAN 2002-225097 08/01/2002 *T.D*
 JAPAN 2002-236036 08/13/2002 *T.D*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 11	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>T.D</i>				

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TITLE
 Method for manufacturing semiconductor device

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